

Appl. No. 09/784,850  
Amdt. Dated July 7, 2004  
Reply to Office action of April 08, 2004  
Attorney Docket No. P14282/040091-003  
EUS/J/P/04-6147

### REMARKS/ARGUMENTS

#### 1.) Claim Amendments

Claims 1-25 are pending in the application. Favorable reconsideration of the application is respectfully requested in view of the following remarks.

#### 2.) Claim Rejections – 35 U.S.C. § 102

The Examiner rejected claims 1-30 under 35 U.S.C. § 102(b) as being anticipated by Barkhordarian (WO 88/08162). Applicants respectfully traverse the Examiner rejection and seek the Examiner's favorable reconsideration in view of the following remarks.

As disclosed and claimed by the present invention, "a plurality of applications" are formed to create "an application chain" wherein each application performs or provides certain functionalities on received data. As further disclosed, such "applications" include Media Stream Applications (MSAs) associated with providing full sets of speech and data resources (i.e., codecs, EC, CCD, DTI and other hardware resources). In order to ensure that all of the network resources are fully utilized while ensuring that the available capacity is not exceeded, the present invention therefore discloses and claims a method for allocating appropriate applications (MSAs) to different processors in a multi-processor environment.

Accordingly and as further claimed in independent Claims 1 and 16, an application chain comprising at least one application to be performed on the data is built. As data are received for processing, program information for identifying a current application in the application chain to be performed is added to the received data. The data are then forwarded to whichever processor that is available within the multi-processor environment. The assigned processor then processes the data using the current application identified by the program information previously added to the data. When completed, the processed data are once again updated with new program information identifying the next application until the data are processed by all of the applications in the application chain.

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In accordance with the teachings of the present invention, program information is therefore added to the received data to facility the processor to extract such data and to identify the application to be used in further processing such data. Since there is more than one application within an application chain, as one processor finishes its tasks, the data is once again updated with new program information allowing the next available processor to correctly identify the next application to be used.

The Barkhordarian reference, on the other hand, discloses a parallel-processing data transfer system. Accordingly, Barkhordarian teaches a system for communicating data within multiple processor environment. Since the Barkhordarian invention includes a plurality of data processing units connected to each other by a common system bus, it further discloses a buffer device for enabling a data communication pathway between the system bus and the local bus and data transmission logic which functions to implement the actual data transfers between processor units.

However, nothing in Barkhordarian discloses or teaches the recited steps of:

- a) building an application chain comprising at least one application to be performed;
- b) receiving the data;
- c) adding program information to the received data for identifying a current application in the application chain to be performed on the data;
- d) forwarding the data to an available processor in the multi-processor environment;
- e) processing the data on the available processor using the current application identified by the added program information; and
- f) updating the processed data with new program information identifying a next application in the application chain to be performed, if any, wherein the forwarding, processing, and updating steps are repeated until the data is processed by all applications in the application chain.

As allegedly disclosing step (a), the Examiner stated that the Abstract of Barkhordarian disclosed "various tasks performed serially specific portions which

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perform specific portions which perform specific computational tasks." Applicants respectfully submit that the Abstract of Barkhordarian merely states that the "timing sequence for the various computational routines or task can be performed serially or in parallel or a combination therefore by the PC" and simply fails to disclose the recited step of building an application chain comprising multiple applications. In other words, Barkhordarian fails to disclose the recited step of building an application chain with at least one application.

Furthermore, the cited reference also fails to anticipate or render obvious recited step (c) as reproduced above. Nothing in Barkhordarian discloses or teaches the step of adding program information identifying the current application to be performed into the received data and then, in accordance with step (d), forwarding the received data to an available processor for further handling. Again, the Examiner cited page 9, lines 5-20 of the Barkhordarian reference as allegedly disclosing these steps. However, Applicants respectfully submit that the cited portion of Page 9 merely discloses a "compiler program" for configuring the operating system by providing a medium for the user to supply the application programs and by formulating the lookup tables of the communications rules in response to input from the user. However, nothing in Barkhordarian discloses the step of adding "program information" to the data itself to identify the "current application" to be performed by the assigned processor.

While further rejecting independent claims 1 and 16, the examiner further stated that the "step of identifying the next application is inherent because, without that there is no data flow logic." Applicants respectfully submit that, *assuming arguendo* that the Examiner is correct in stating that such step of identifying is inherent, Barkhordarian still fails to disclose the step of performing such a step by "updating the processed data with new program information identifying a next application in the application chain." Accordingly, even if the general step of identifying the next application is inherent, the cited reference nevertheless still fails to anticipate or render obvious the novel step of "updating

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the processed data with new program information" to allow the next processor to identify the next application to be performed.

Lastly, since no application chain has been disclosed or taught by Barkhordarian, the cited reference similarly fails to disclose or teach the recited step of repeatedly performing the recited steps until all of the applications in the application chain have been processed.

As a result, Applicants respectfully submit that the cited reference fails to anticipate or render obvious the presently pending independent claims and a favorable reconsideration is earnestly requested.

**3.) Claim Rejections – 35 U.S.C. § 103 (a)**

The Examiner rejected claims 2-15 and 17-30 under 35 U.S.C. § 103(a) as being unpatentable over Barkhordarian in view of Brendenberg (U.S. Pat. No. 5,826,253). In view of the above arguments and since all of these claims are dependent on now allowable independent claims reciting further limitations and elements, a notice of allowance for all of the pending claims is respectfully requested.

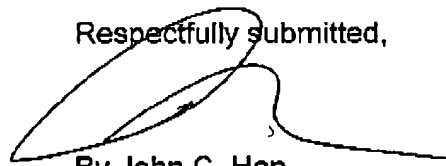
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### CONCLUSION

In view of the foregoing remarks, the Applicant believes all of the claims currently pending in the Application to be in a condition for allowance. The Applicant, therefore, respectfully requests that the Examiner withdraw all rejections and issue a Notice of Allowance for all pending claims.

The Applicant requests a telephonic interview if the Examiner has any questions or requires any additional information that would further or expedite the prosecution of the Application.

Respectfully submitted,



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